

IN THE ABSTRACT OF THE DISCLOSURE

Please amend the Abstract by rewriting same to read as follows.

~~The present invention relates to a~~ A device for performing predetermined processing on ~~[[a]]~~ an input signal ~~inputted thereto~~ that may have a signal amplitude of more than one bit~~[[,]]~~. The input ~~which~~ signal is obtained by subjecting one-bit serial signals to predetermined signal processing, wherein the signal amplitude of more than one bit is converted to a one-bit serial signal by accumulating the signal ~~amplitude exceeding that of~~ amplitudes that exceed one bit, delaying the accumulated signal on the basis of the input signal, and outputting the accumulated signal.